

<b>Notice of References Cited</b>	Application/Control No. 10/643,577	Applicant(s)/Patent Under Reexamination FAANES ET AL.	
	Examiner SHANE M. THOMAS	Art Unit 2186	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,336,168	01-2002	Frederick et al.	711/141
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Collins et al., "Hardware Identification of Cache Conflict Misses," IEEE, 32 <sup>nd</sup> Annual International Symposium on Microarchitecture, 11/18/1999, pp.126-135.
	V	
	W	
	X	

\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.